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<b>TRANSMITTAL FORM</b> (to be used for all correspondence after initial filing)		Application Number	10/766,899
		Filing Date	January 30, 2004
		First Named Inventor	Kenji ICHIKAWA
		Group Art Unit	2814
		Examiner Name	Phat X. Cao
Total Number of Pages in This Submission		Attorney Docket Number	740165-371

ENCLOSURES (check all that apply)		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Jeffrey L. Costellia, Reg. No. 35,483 Nixon Peabody LLP 401 9 <sup>th</sup> Street, N.W. Suite 900 Washington, D.C. 20004-2128
Signature	
Date	7/25/2005

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: )  
Kenji ICHIKAWA ) Group Art Unit: 2814  
Serial No. 10/766,899 ) Examiner: Phat X. Cao  
Filed: January 30, 2004 )  
For: SEMICONDUCTOR DEVICE AND )  
SEMICONDUCTOR INTEGRATED )  
CIRCUIT DEVICE )

**RESPONSE TO RESTRICTION/ELECTION REQUIREMENT**

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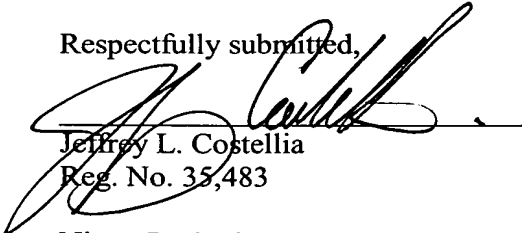
Sir:

In response to the Office Action dated June 24, 2005, Applicant hereby elects the species of Group II, a semiconductor device has the wiring portion integrally intersects with the coupling portion and has no field oxide film disposed between the first and second MOS transistor groups as disclosed in Fig. 3, with traverse.

Specifically, Applicants contend that claims 1-5 and 8-14 are readable on elected Group II: Figure 3 (Embodiment 2). Further, however, Applicants assert that claims 1-5 and 9-14 should be considered generic to both Group I and Group II. As a result, should any of the generic claims 1-5 and 9-14 be allowed, then claims 6-8 should like be allowed.

Should the Examiner believe a conference would be of benefit in expediting the prosecution of the instant application, he is hereby invited to telephone counsel to arrange such a conference.

Respectfully submitted,

  
Jeffrey L. Costellia  
Reg. No. 35,483

Nixon Peabody LLP  
401 9<sup>th</sup> Street N.W., Suite 900  
Washington, D. C. 20004  
(202) 585-8000